WEST Search History

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DATE: Thursday, June 09, 2005

Hide?	Set Name	Query	Hit Count
		DB=USPT; PLUR=NO; OP=ADJ	
☐	L115	l113 with L114	30
☐	L114	multi-threading or multithreading or thread$1	237806
☐	L113	packet process$3	3592
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L112	l6 with l38	148
☐	L111	suspend$3 same L110	3
☐	L110	L6 same L38	194
☐	L109	L6 and L38	752
☐	L108	L100 same L106	3
☐	L107	L100 and L106	105
☐	L106	L38 with suspend$3	2252
☐	L105	L103 and L104	3
☐	L104	L1 and L101	288
☐	L103	packet$1 and L102	40
☐	L102	L38.ti,ab,clm. and L100.ti,ab,clm.	129
☐	L101	L38 and L100	4991
☐	L100	router$1	51745
☐	L99	L89 and L98	6
☐	L98	(712/23).ccls.	694
☐	L97	L65 and L94	1
☐	L96	L49 and L94	1
☐	L95	L89 and L94	4
☐	L94	(712/241).ccls.	240
☐	L93	L70 and L89	0
☐	L92	L70 and L91	0
☐	L91	L38 and L89	1064
☐	L90	L65 and L89	19
☐	L89	packet switch$3	18844
☐	L88	sun.as. and treat.xp. and L38	8
☐	L87	L38 same L86	33
☐	L86	instruction block$1	1919

☐	L85	L38 and L83	5
☐	L84	packet$1 and L83	13
☐	L83	decoupled processor$1	57
☐	L82	L81 adj processor$1	5
☐	L81	non-blocking or nonblocking	6578
☐	L80	mon-blocking or nonblocking	1243
☐	L79	L38 with L65	17
☐	L78	L38 adj processor	671
☐	L77	L65 and L70	1
☐	L76	L62 and L70	3
☐	L75	L70 and L74	1
☐	L74	(712/32).ccls.	362
☐	L73	context switch$3 same L59	30
☐	L72	L59 and L70	1
☐	L71	L49 and L70	0
☐	L70	(712/235).ccls.	175
☐	L69	packet$1.ti,ab,clm. and L68	8
☐	L68	L65 with (parallel or pipelin$3)	132
☐	L67	packet$1.ti,ab,clm. and L65	73
☐	L66	packet.ti,ab,clm. and L65	59
☐	L65	subprocessor$1 or sub-processor$1	1077
☐	L64	L38 same L62	29
☐	L63	packet$1 same L62	17
☐	L62	non-blocking with processor$1	336
☐	L61	L60 not L58	5
☐	L60	L59 and L53	14
☐	L59	packet with processor$1	11947
☐	L58	L49 and L53	9
☐	L57	packet$1 and L52	22
☐	L56	L52 and L55	8
☐	L55	conditional branch$3	5212
☐	L54	L52 and L53	0
☐	L53	(712/234).ccls.	336
☐	L52	serial processors	98
☐	L51	L38 with L49	26
☐	L50	L38 and L49	167
☐	L49	packet processor$1	1494

☐	L48	L47 not private branch exchange	35
☐	L47	L45 not L46	46
☐	L46	branch$3 same L44	5
☐	L45	branch$3 and L44	51
☐	L44	L38 same L41	236
☐	L43	branch$3 and L42	471
☐	L42	L38 and L41	2693
☐	L41	decoupled	27354
☐	L40	L38 and L39	5
☐	L39	decoupled architecture	30
☐	L38	multi-threading or multithreading or thread$1	292691
☐	L37	chained processor$1	8
☐	L36	L28 and L35	0
☐	L35	L34 with buffer$1	0
☐	L34	reconfigurable pipeline$1	89
☐	L33	L29 and L32	2
☐	L32	709/$.ccls.	36796
☐	L31	router$1 and L29	17
☐	L30	L28 and L29	2
☐	L29	reservation station$1	1013
☐	L28	internet protocol or tcp	64244
☐	L27	L25 and L26	18
☐	L26	reservation station$1 with branch$3	322
☐	L25	internet protocol or ip or tcp	120655
☐	L24	reservation station$1 and L23	10
☐	L23	(data or network) adj packet$1	45419
☐	L22	(router$1 or switch$2) and L21	80
☐	L21	packet$1 and reservation station$1	149
☐	L20	L18 same next instruction	18
☐	L19	pipeline stage$1 with conditional branch$3	105
☐	L18	pipeline stage$1 with branch$3	490
☐	L17	L16 not L10	54
☐	L16	stall$3 and L15	54
☐	L15	L6 same thread$1	191
☐	L14	L11 and L6	1
☐	L13	serial sub-processors	0
☐	L12	serial subprocessors	0

☐	L11	serial processors	98
☐	L10	L6 and L9	13
☐	L9	stall$3 same L1	82
☐	L8	stall$3 and L7	103
☐	L7	L1 and L6	232
☐	L6	packet process$3	7414
☐	L5	conditional branch$3 same L4	6
☐	L4	new thread	1557
☐	L3	conditional branch$3 same L1	20
☐	L2	conditional branch$3 with L1	2
☐	L1	context adj (switch$3 or swap$4)	3938

END OF SEARCH HISTORY